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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):

Soucy et al.

**SERIAL NO.:** 

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**GROUP NO.:** 

3652

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**EXAMINER:** 

S. Bratlie

TITLE:

Apparatus and Methods for Semiconductor Wafer Processing Equipment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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## **ASSOCIATE POWER OF ATTORNEY**

FFD 2 C SSS

Sir:

An associate power of attorney is hereby granted to:

**GROUP 3600** 

NAME	REG. NO.
Andrew L. Jagenow	51,842

in connection with the above-identified patent application.

Please continue to direct all correspondence relating to the above application to:

Patent Administrator Testa, Hurwitz & Thibeault, LLP 125 High Street Boston, MA 02110

Respectfully submitted,

Date: February 17, 2004

Reg. No. 42,545

Tel. No.: (617) 248-7675

Fax No.: (617) 248-7100

John Y. Forcier

Attorney for the Applicants

Testa, Hurwitz, & Thibeault, LLP

125 High Street

Boston, Massachusetts 02110